

Notice of References Cited	Application/Control No. 10/792,168	Applicant(s)/Patent Under Reexamination VORBACH ET AL	
	Examiner Gopal C. Ray	Art Unit 2111	Page 1 of 1

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*	B	US-6,437,441	08-2002	Yamamoto, Hiroshi	257/758
*	C	US-6,211,697	04-2001	Lien et al.	326/41
*	D	US-6,721,830	04-2004	Vorbach et al.	710/100
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NON-PATENT DOCUMENTS

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	U	"Programmable logic machine (A programmable cell array)" by Skokan, Z.E. (abstract only) Publication Date: Oct. 1983
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.